

PATENT NUMBER

Best Available Copy

U.S. UTILITY Patent Application

PATENT DATE

Q.A

CONT/PRIOR

SUBCLASS

EXAMINER

GEORGE, K.

-370

458

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David Juracek
Edward Evans

2663

Distributed time-multiplexed bus architecture and emulation apparatus

PTO-2040
12/89[illegible]☐ Continued on Issue Slip Inside File Jacket

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